





### **Grid Emulation through Power Electronics Hardware Testbed**



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#### **About UTK Electrical Engineering**



- UTK EECS Department (web.eecs.utk.edu) ~50 faculty ~250 Graduate Students ~900 Undergraduates
- CURENT NSF/DOE-sponsored Engineering Research Center on power transmission
- PoTenntial Graduate research traineeship in wide bandgap power electronics
- Oak Ridge National Laboratory
  - Advanced Power Electronics and Electric Machines
  - Utility and Building Power System Applications

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- A nation-wide transmission grid that is fully monitored and dynamically controlled for high efficiency, high reliability, low cost, better accommodation of renewable sources, full utilization of storage, and responsive load.
- A new generation of electric power and energy systems engineering leaders with a global perspective coming from diverse backgrounds.







## **UTK Power Electronics**

- Five full-time power electronics faculty
  - Daniel Costinett, Fred Wang, Leon Tolbert, Kevin Bai, Helen Cui







- 40 Graduate students in power electronics
- ~80 Graduate students in CURENT (Power Elec./Systems)
  - One of the largest graduate power programs in the US
- Close collaboration with Oak Ridge National Laboratory
- Focus on hands-on, design-oriented coursework and training













## **POTENNTIAL WBG PE Graduate Traineeship**

- DOE-funded traineeship in wide bandgap power electronics
- \$2.9 Million, 6-year DOE sponsorship one of only two awardees in the nation (Virginia Tech)
- Funds U.S. citizen graduate students (25 graduates and 10 current students in program)
- Hands-on coursework and research leveraging WBG
- Emphasizes internships with industry & national laboratories
- PoTENNtial.eecs.utk.edu









## **Power Electronics Facilities**

- UTK, as part of the NSF-DOE Engineering Research Center CURENT, boasts one of the largest and best-equipped university power electronics laboratories in the nation.
- Multimillion-dollar state-of-the-art laboratory contains all electrical, mechanical, and thermal equipment and testing infrastructure necessary to characterize, model, design, construct, test, and control power electronics up to 1 MW and 40 kV.
- A new lab, opened in 2020 contains equipment for power module fabrication.



Low and Medium Power Lab





Example Projects



Packaging Lab Equipment



Control and Visualization Lab



**High Power Lab** 

## **Power Electronics Research Applications**



### **U.S. Wind and Solar Resources**



Best wind and solar sources are far from load centers.

**Distance provides diversity of sources.** 

Transmission networks must play a central role in integration.







## **CURENT Concept**



- High penetration of renewable energy sources
- Flexible DC and AC transmission
- Accommodate load and source variability, responsive load
- Improved situational awareness, ultra-wide-area control



#### Hardware Grid Emulation System Testbed (HTB)



- <u>Emulate various grid scenarios</u> with interconnected clusters of scaled-down generators, loads, and energy storage.
- Demonstrate tools developed by research thrusts.



#### CURENT Hardware Testbed (HTB) - Emulation Structure



- Hardware Testbed (HTB) uses modular, reconfigurable power electronic converters to emulate power system components, and circulate power between them
- Emulate various grid scenarios with interconnected clusters of scaled generators and loads





## Hardware Testbed (HTB) Background

- Power circulates within a single area
  - Inverters exchange power between DC bus and the emulated power system
  - DC bus makes up losses in the emulated system and inverters





## **Hardware Testbed Architecture**



Hardware cabinets and software are designed to make entire system modular. Multiple simultaneous control functions and software hierarchy established.

Several generation/load cabinets and transmission line cabinets so larger system can be emulated.

Energy storage and responsive load control key to grids with high penetration of renewables.

Virtual synchronous generator control of wind turbine converters and PV converters.

Transmission line fault emulation, FACTS, and power system protection function emulation.

Realized different <u>scenarios</u> with high penetration renewables and HVDC overlay.

#### Communication, Control, and Visualization (4 areas)



#### **Control Center functions**

Area control center:

- Control local area
- Independent from each other
- Dispatch transmission lines
- Implemented with AGC, local state estimation, voltage monitoring, etc.

#### Central controller:

- Only for automatic scenario sequencing and demonstration
- Future system level testing

#### Visualization computer:

• Only for display of system information on the video wall



### **Reconfigurable Software and Hardware**

- All Type I (area) and Type III (long ac lines or dc lines) cabinets are connected to Type II (variable inductor) cabinets
- · Local line impedance can be controlled to the desired value
- Different system topologies reconfiguration are through front panels of variable inductor cabinets
- Each area and central controller has identical structure which can control any area cabinet



Reconfigurable HTB system



Type II cabinets

## North American Grid with HVDC Overlay



North American CURENT system with WECC, EI, and ERCOT systems connected via multi-terminal HVDC overlay, and high penetration of renewable energy sources





## **National Grid HTB Demonstration Scenarios**

- Demonstrate security/stability assessment based HTB control system
- Demonstrate ability of HVDC overlay to provide continent-level coordination between the three grids: EI, ERCOT, WECC
- Evaluate high penetration (>80%) of renewables in

**RENT** 



## **Emulator Development Summary**



#### **Solar Power Emulator**



## **Battery Energy Storage Emulator**



## Wind Turbine Emulator



#### Virtual Synchronous Generator (VSG) Control of **Type-4 Wind Turbine**

**Objective:** Let renewable energy sources behave like the synchronous generators in power system

#### Composis disbatele coMSG MPPTder and abate inviored by MPRRT



and provide inertial response

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#### Machine Side Converter

controls wind turbine speed

MPPT

#### Follow grid demands



**Dispatch-able** 

#### Minute Level **Energy Storage**

Provide energy buffer



#### **VSD Loads Hierarchical Control for Grid Frequency Support**



Aggregated variable speed drives (VSDs) offer opportunity to make short-term load changes to provide frequency regulation services to the grid.

CURENT

#### **Converter-Based Emulator of Data Center Power Supply System**

1. Typical structure of a data center ac power distribution system 3-Ф 277 Vac 12 Vdc DC/AC sag transient condition. 3 VRM 🕻 📢 3 PFC 208 Vac active eco mode double conversion mode Bus Vt(p.u.) 0 0 Utility **PSU** Critical load PDU Emulation result  $\frac{1}{20\%}$  votlage drop for 1s Matlab simulation result w/ 20% votlage drop for 1s Battery UPS Vdc 0.8 12 1.4 1.6 VRM 1-4 3 PSU Vdc(p.u.)PSU Vac(p.u.) Bus Id(p.u.) PFC 208 Vac 2. Linearized average modeling **PSU** Non critical load PDU including closed loop controls 0.6 0.8 1.2 1.4 1.6 3. Emulator of data Data center power system emulator center system Vt,abc implemented in HTB 0.8 12 1.6 0.6 1.4 PLL & dq0/abc transfromation Vt dq,  $\theta$ Power saglends sagistarts UPS in Data center power network system model emulated in Bus 10 12 0.8 1.4 1.6 0.6 1 HTB iref\_dq Time (s) Inner current CURENT gabc loop

4. Dynamic performance with 20% voltage

1.8

1.8

1.8

1.8

## **Bus Short-Circuit Fault Emulation**



Realize short-circuit faults by controlling the corresponding phase of emulator terminal voltages to zero



Control block diagram of double-line-to-ground fault



Voltages of three-phase short circuit



Currents of three-phase short circuit







Single-line-to-ground short circuit with 1  $\Omega$  grounding resistance



## **AC or DC Transmission Line Emulator**



Back-to-back structure with two terminals





#### Transmission Line Emulator Attributes

- · Vary the line length (impedance) for different scenarios
- Short circuit or open line faults
- Reclosing emulation
- Emulate multiple parallel lines
- Emulate FACTS applications such as CVSR





## **Transmission Line Fault Emulation**



#### **Transmission Line with Combined FACTS Emulation**



## **RTDS Interface with HTB**



#### **Interoperability: Multiple Simultaneous Control Functions**



#### **3-Area CURENT HTB Structure**



Three-area system including multi-terminal HVDC and wind farm





#### Three-area system:

- Three areas interconnected by three long transmission lines (220 km, 220 km, and 132 km respectively)
- Area 3 connected to a multi-terminal HVDC (MTDC) system with DC lines (two 100 km, one 60 km, and one 70 km)

#### **Scenario - High Renewable Penetration – Inertia Emulation**

#### **Scenario**

• Replace G2 by an onshore wind farm.

Together with offshore wind provided by HVDC, system <u>renewable penetration can reach 80%</u>. Event triggered by a <u>HVDC converter failure</u>.

#### Solution

- Frequency and voltage support from onshore wind farm and the HVDC converters
- Curtailment and voltage mode control when necessary
- Integration of energy storage to further enable grid support controls

#### **Expected outcome**

 System frequency and voltage within acceptable range



- Base case with generator
- MPPT
- Voltage mode
- Voltage mode with storage

#### HTB scenario test results



#### **Scenario – Wide-area Damping Controller**

#### Scenario

- 50% renewable penetration
- One poorly damped inter-area oscillation mode between Area 1 and Area 2

#### • Wide-area Damping Controller (WADC)

- Based on the measurement-driven model, which is identified by using ringdown data and ambient data
- Controller input: frequency difference between Area 1 and Area 2
- Controller output: added to exciter voltage reference of Generator 1
- System disturbance
  - Load increase (Bus 9, 0.7 p.u. to 1.1 p.u.)





#### Synchronized Cyber Attack and Detection on HTB

#### Vulnerabilities and Threat Model

- Protocol IEEE C37.118 has poor security mechanisms 0 and is vulnerable to cyber attacks such as eavesdropping, frame manipulation, interruption, and bad data injection.
- Attacker is assumed to be able to compromise a large 0 portion of PMUs in the phasor network but not all PMUs; implemented by TCP hijacking on HTB.
- Assume that the attack and real event will not happen at 0 the same time.



#### **Multiple Event Analysis**

- Proposed a multiple-event analysis based on sparse coding 0 to detect and recognize multiple, involved single events, i.e., generator trip, loading shedding and line trip. 100% detection accuracy can be achieved on single event detection.
- When a real event happens, all buses should respond 0 correspondingly, Also, detection results on different buses



#### **Attack Detection**

Voting scheme is used to perform attack detection. 0 Events that can only be detected in less than 50% of the PMUs are classified as false events that were launched by an attacker.

#### Scenario – Measurement-Based Voltage Stability Assessment and Control for Load Area (3-Area)

- Collapse scenario: load increase on Bus 13 until system collapses.
  Stable scenario: with the same load increase, the remedial action (Q support from MTDC) is
  - the remedial action (Q support from MTDC) is taken against the voltage instability triggered by the stability margin monitoring.





#### **HTB 4-Area WECC Structure**



#### HTB Demo Scenario – State Prediction and Control to Prevent Transient Instability

# Using a State Predictor for proactive, controlled separation against transient instability:

- Based on real-time state estimation, the State Predictor calculates generator states semi-analytically for a future time window.
- After a line trip between buses 2 and 8, once the angle difference between G2 and G7 is predicted to reach a threshold, separate the system and stabilize frequencies by HVDC.





#### Emulation of Aggregated VSD to Provide Grid Frequency Regulation

- Variable speed drives (VSDs) control motor speed and are increasing in penetration because of the system efficiency gains typically have < 2 year payback.
- Many VSD loads (pumps and fans) can provide frequency regulation service (FRS) by adjusting motors' reference speed temporarily.



### Power Electronics Interfaced Load Modeling in Transient Stability Simulator

- The developed power electronics (PE) interfaced load for transient stability (TS) studies emphasizes electromechanical transients and oscillations in the context of large-scale power network.
- TS model of electric vehicle (EV) charger is created as a typical PE interfaced load. EV charger TS model is suitable for large-scale TS analysis focusing on relatively slow dynamic of PE devices.
- > EV charger modeling principle in TS simulator:

EV charger equivalent TS model is developed based on the detailed electromagnetic transient (EMT) model according to reasonable simplification:



Model verification: EV charger load subject to terminal bus voltage sag:

Integrate the EV charger load into IEEE 9 bus system. A significant terminal voltage sag occurs at t = 2.5 s and last for 0.2 s.



#### **Demo 1: System Security Framework: Stability in Late Afternoon**



- 1. In late afternoon, PV generation is decreasing and other generation rapidly increasing.
- 2. Trip some generation at LA area, local generation sources are not able to handle the ramping rate of the load
- 3. Use HVDC to transfer the power from the north to support the load



Figure 4. CAISO's generation breakdown for April 24, 2016. Illustration from CAISO

## **HTB Microgrid Controller Testing**

Microgrid with Dynamic Boundary by Sectionalizing Smart Switches



#### Controller Interface for HTB Tests



Oscilloscope Measurements of Boundary Change Tests



## **HTB Microgrid Controller Testing**

#### Microgrid with Dynamic Boundary by Sectionalizing Smart Switches









#### Microgrid Controller for Distribution System with Multiple Islands and Dynamic Boundary



- CURENT developed a microgrid controller implemented on EPB's electrical distribution system in Chattanooga
- Controller first implemented on HTB to check its functions:
  - PQ balance
  - Black start
  - Planned and unplanned islanding
  - Dynamic boundary with multiple islands
  - Resynchronization and reconnection

## **Hardware Test-bed Advantages**

Broad time scales in one system - microseconds for power electronics to miliseconds and seconds for power system event.

Integrate real-time communication, protection, control, and power (and cyber security).

Multiple power electronic converters (for wind and solar and energy storage) with separate controls.

Capable of testing actual communication and measurements.

A useful bridge from pure simulation to real power system application.



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ISO

EAKRELIABILITY











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